

FIG.1

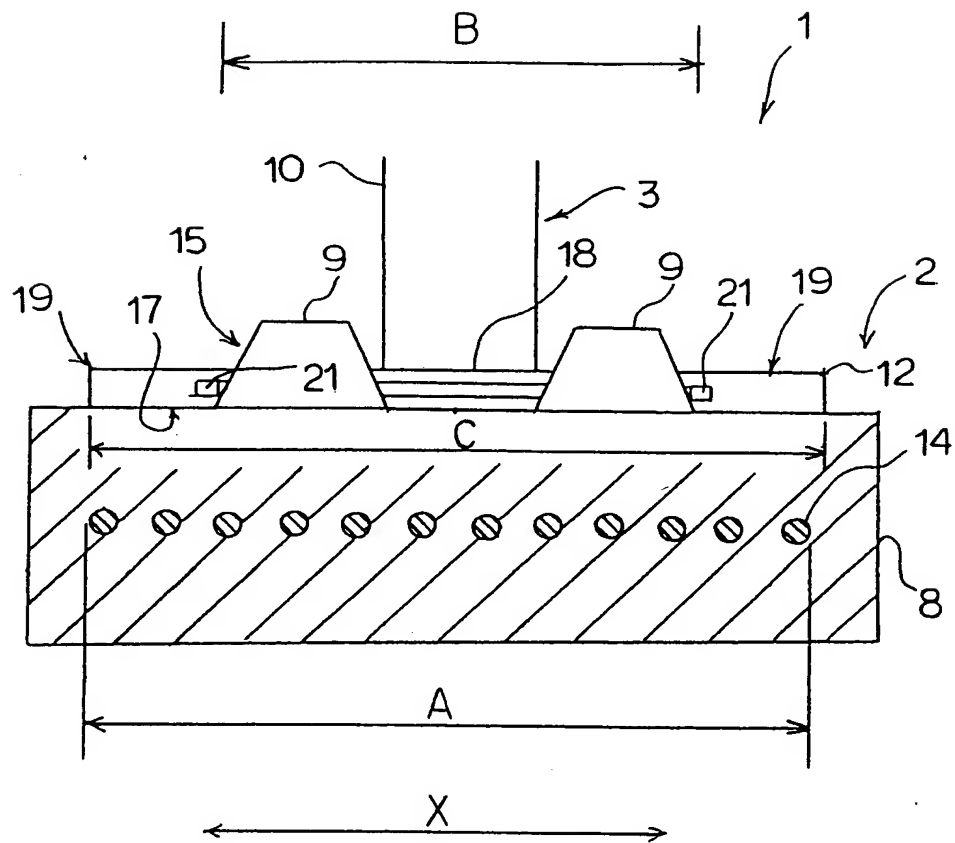


FIG.2

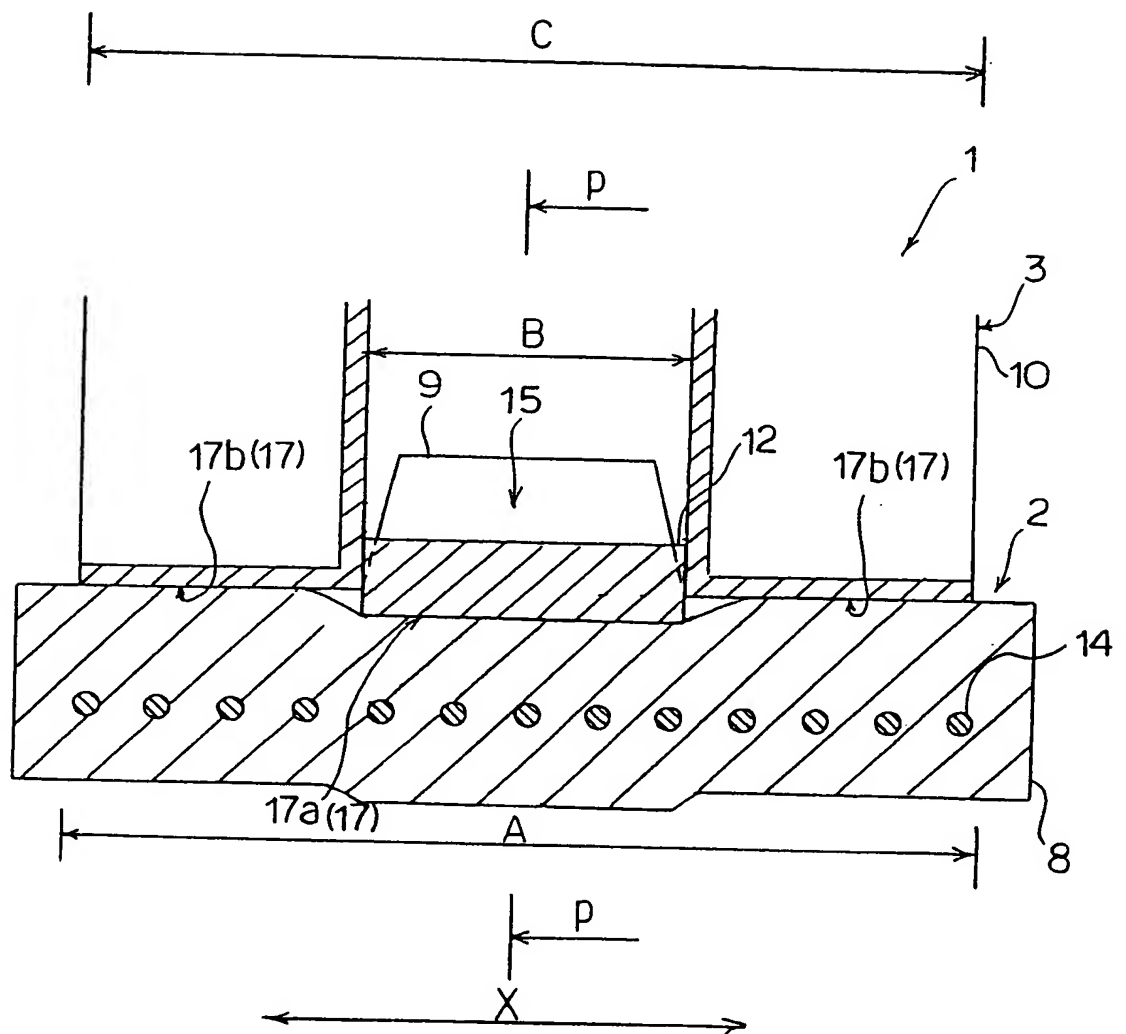


FIG.3

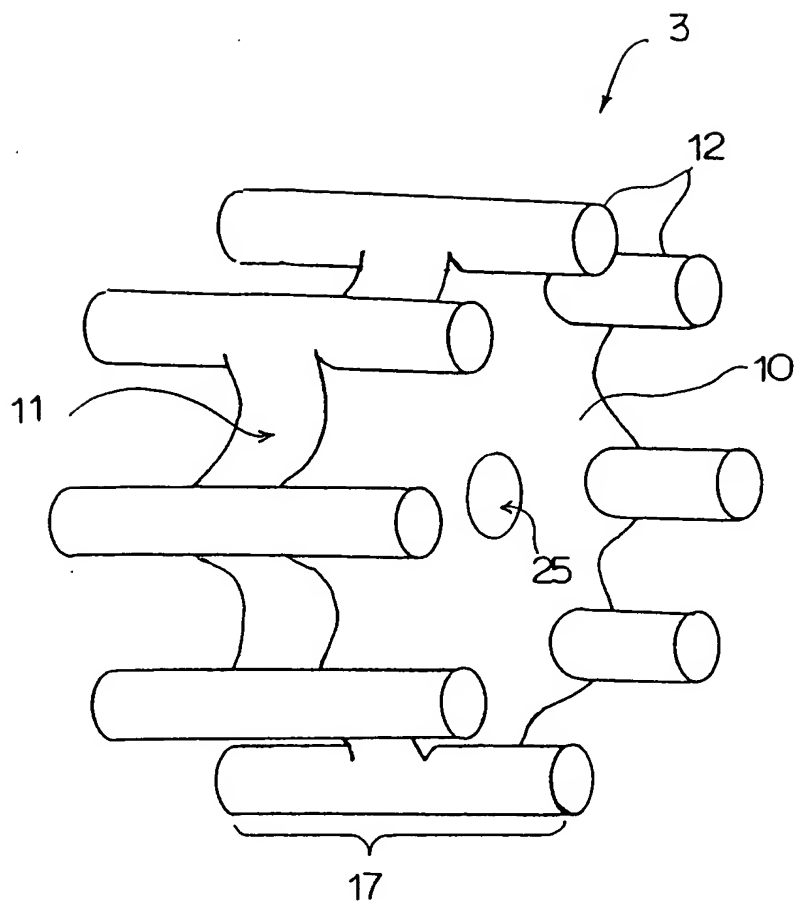


FIG.4

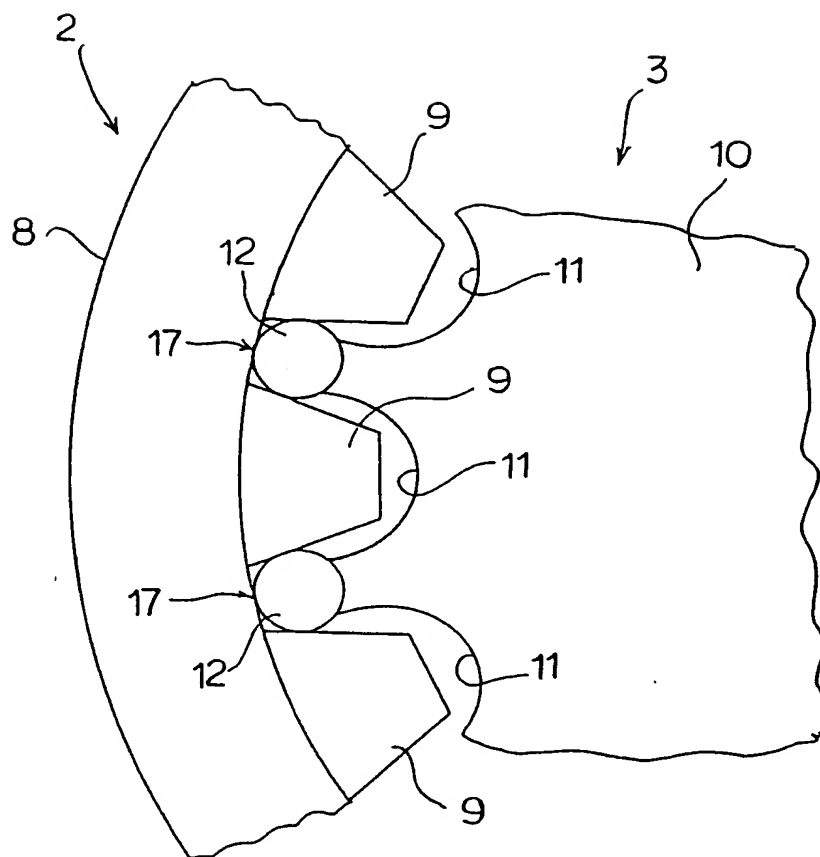


FIG.5

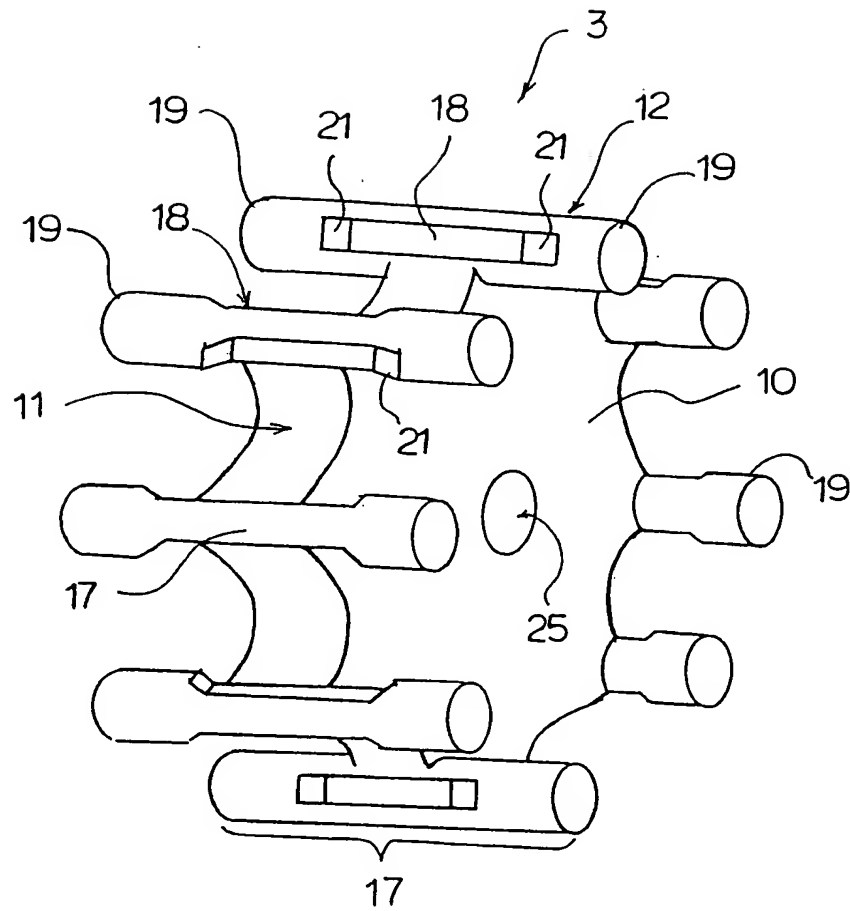


FIG.6

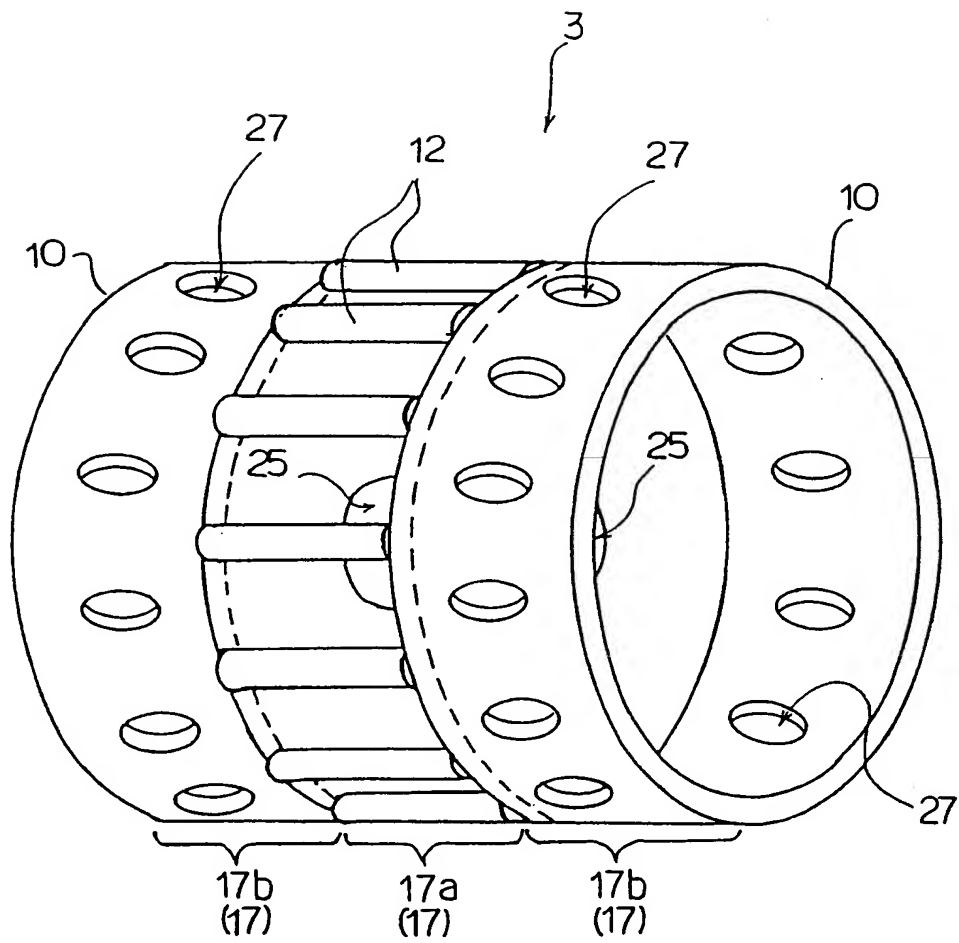


FIG.7

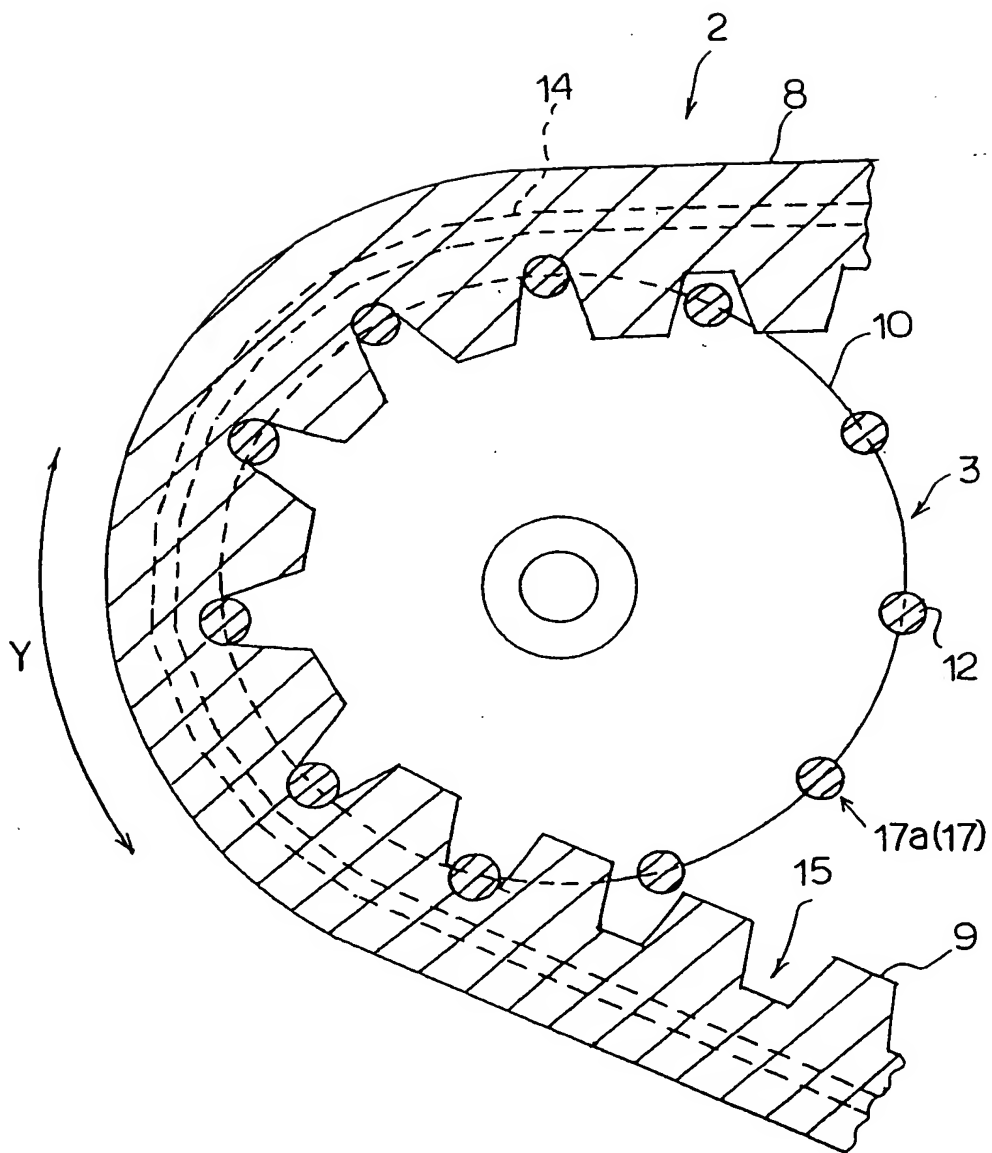


FIG.8

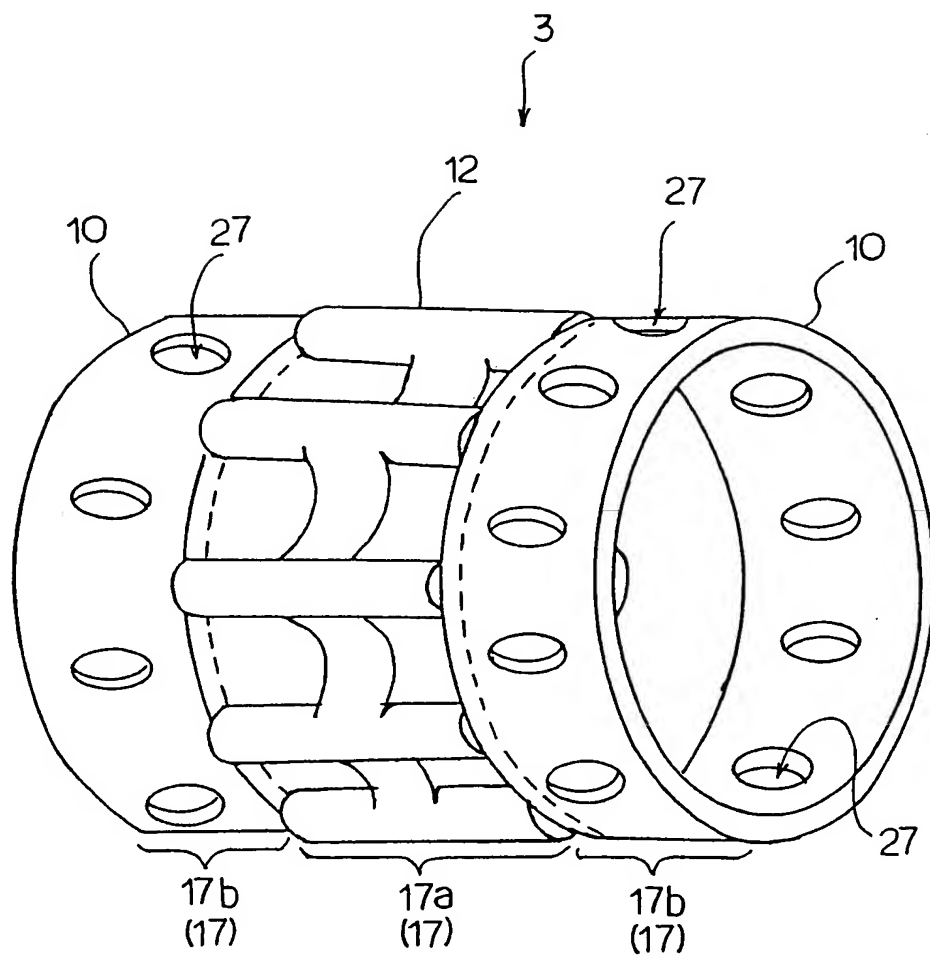


FIG. 1 is a schematic diagram of a rectangular substrate 2 with a wavy top edge. It contains three vertically stacked rectangular openings 18. The openings are separated by horizontal dividers 15. The openings are connected to a common vertical channel 12 (3) on the left side. The openings are labeled 18, 15, and 18 from top to bottom. The dividers are labeled 9. The openings are labeled 19 and 21. A coordinate system with X and Y axes is shown at the bottom right.

FIG.10

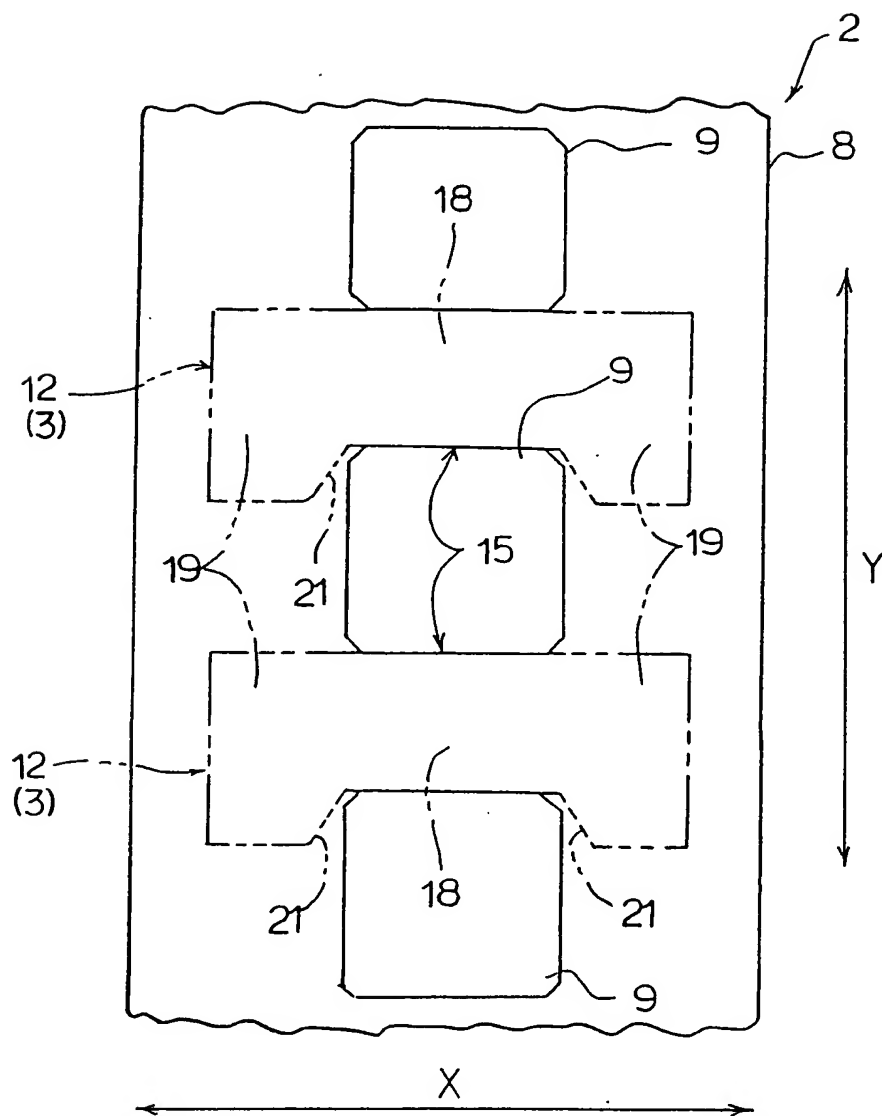


FIG.11

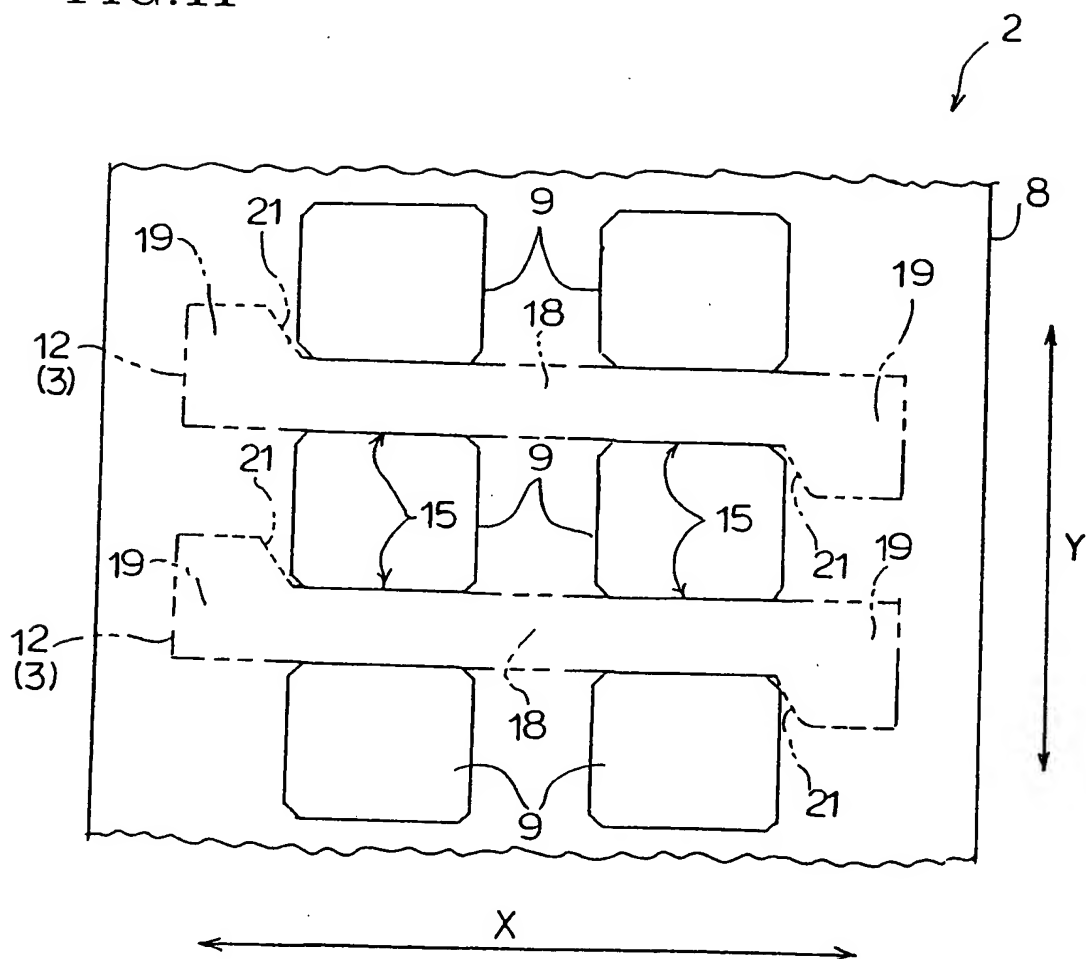


FIG.12

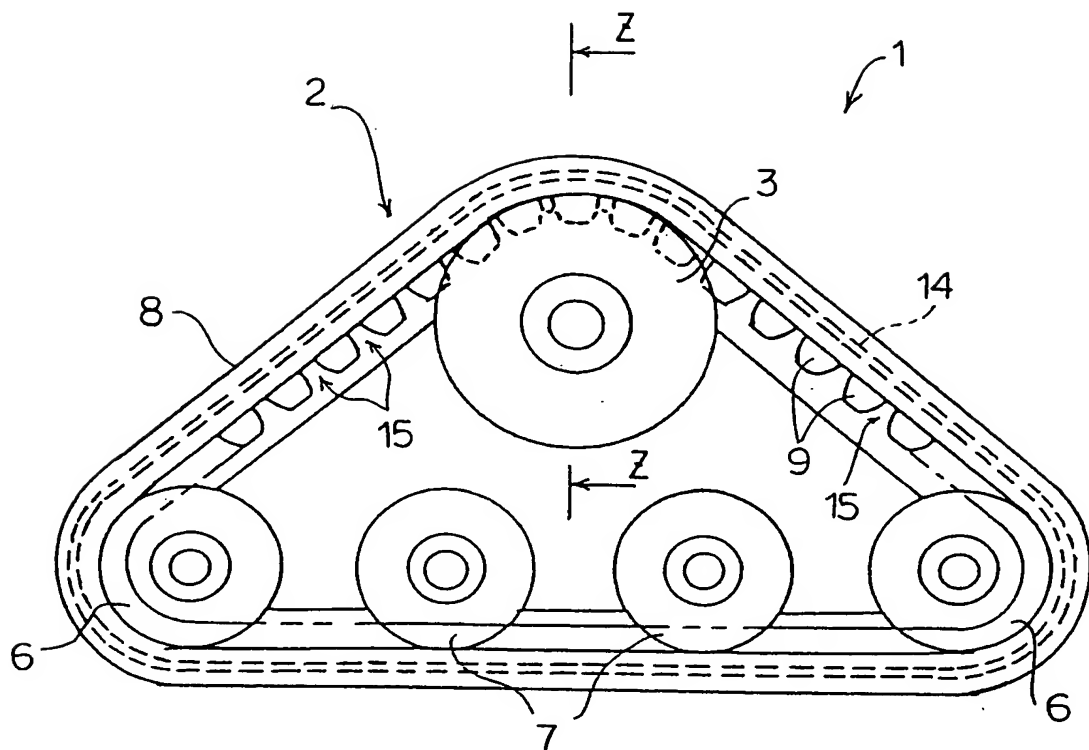


FIG.13
(Prior Art)

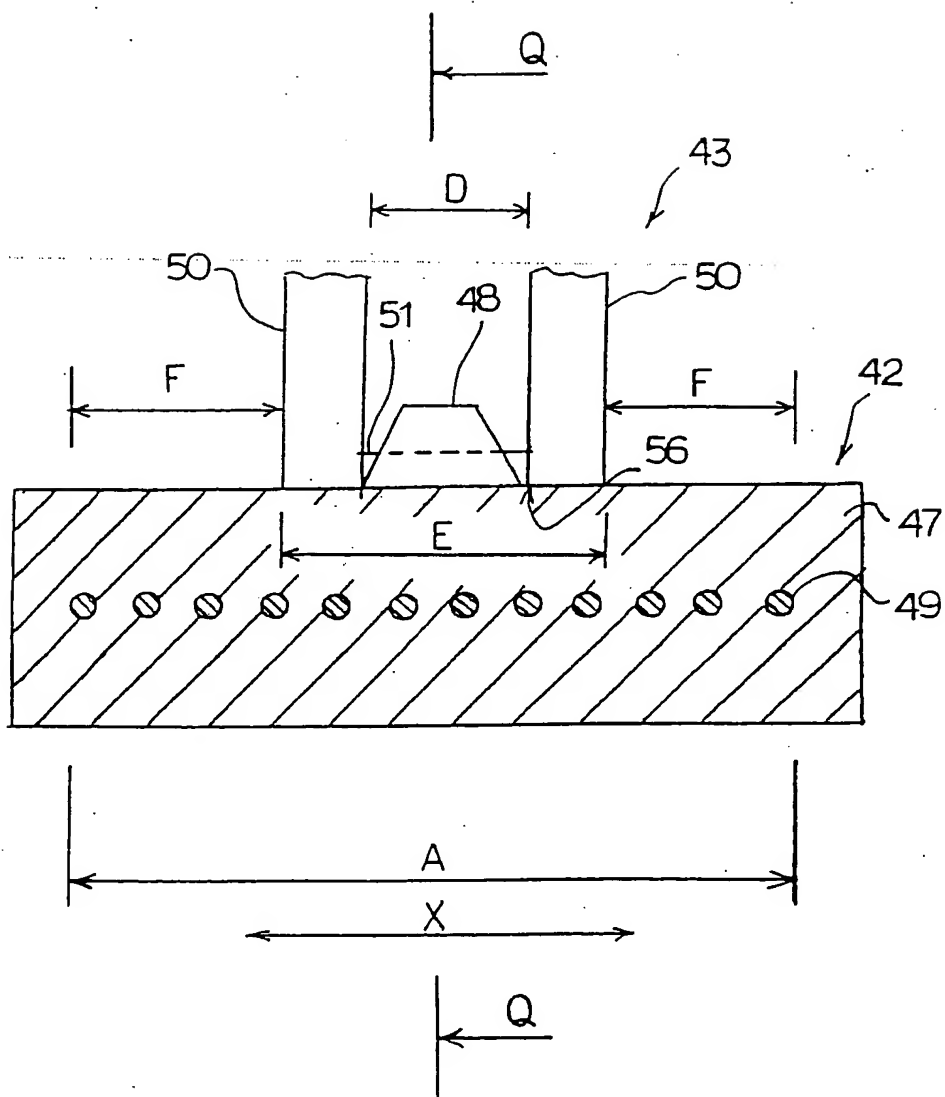


FIG.14
(Prior Art)

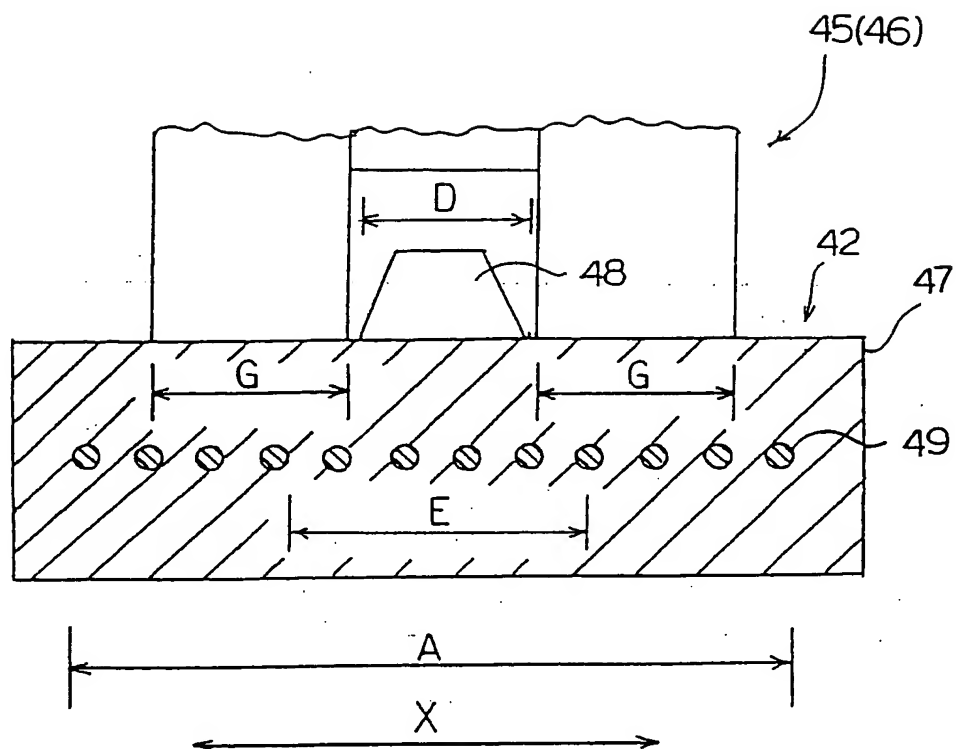


FIG.15
(Prior Art)

